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 Mitsuyama, Y.; Andales, Z.; Onoye, T.; Shirakawa, I.;
[Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002 12-15 May 2002 Page\(s\):151 - 154](#)
 Digital Object Identifier 10.1109/CICC.2002.1012786
Summary: "Burst mode" is a new cipher mode, which is devised dedicatedly for performance implementation of Advanced Encryption Standard (AES) and other 128-bit block cipher algorithms. In comparison with the conventional modes, the
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 Daemen, J.; Claesen, L.; Genoe, M.; Peeters, G.; Govaerts, R.; Vandewalle, J.
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Summary: The design of a high-speed cryptographic coprocessor is presented. The coprocessor is named Subterranean and can be used for both cryptographic processing: sequence generation (Substream) and cryptographic hashing (Subhash). In Subterranean the c.....
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 Goodman, J.; Dancy, A.P.; Chandrakasan, A.P.;
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Digital Object Identifier 10.1109/4.726580

Summary: Security concerns for battery-operated wireless systems require the energy-efficient data-encryption techniques that can adapt to the time-varying quality-of-service requirements inherent in a wireless application. This

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 Chakraborty, A.; Macii, E.; Poncino, M.;
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 Digital Object Identifier 10.1109/ISSCS.2005.1509840
Summary: LCD display consumes a significant chunk of energy in various por which are typically battery operated and support streaming video. Due to the ri copying of perfect digital uncompressed data travelling on DVI interface, the....
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- ☐ **6. High performance cryptographic engine PANAMA: hardware implementa**
 Selimis, G.; Kitsos, P.; Koufopavlou, O.;
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 13-15 Dec. 2004 Page(s):575 - 578
 Digital Object Identifier 10.1109/ICECS.2004.1399746
Summary: A hardware implementation of a dual operation cryptographic engir presented. The implementation of the PANAMA algorithm can be used both as and a stream cipher. A basic characteristic of PANAMA is a high degree of par
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- ☐ **7. VLSI cellular array of coupled delta-sigma modulators for random analog generation**
 Cauwenberghs, G.;
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 Digital Object Identifier 10.1109/ACSSC.1997.679085
Summary: Parallel VLSI generation of random analog vectors with controlled s deterministic chaos is the key to applications such as analog encryption and se communications, analog built-in self-test, stochastic neural networks, and simu
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 Claesen, L.; Daemen, J.; Genoe, M.; Peeters, G.;
[Computer Design: VLSI in Computers and Processors, 1993. ICCD '93. Proce-](#)
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 3-6 Oct. 1993 Page(s):610 - 613
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Summary: A high-speed cryptographic coprocessor is presented. This coproci Subterranean and can be used for both cryptographic pseudorandom sequenc (Substream) and cryptographic hashing (Subhash). In Substream mode the ch
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- 9. ASC: A Stream Compiler for Computing with FPGAs**
 Mencer Oskar ;
[IEEE Transactions on Computer-Aided Design of Integrated Circuits and Syste](#)
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Volume PP, Issue 99, 2005 Page(s):1 - 1

Digital Object Identifier 10.1109/TCAD.2005.857377

Summary: ASC, A Stream Compiler for computing with Field Programmable C (FPGAs) emerges from our ambition to bridge the hardware design productivity number of available transistors grows more rapidly than the productivity of VLS

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